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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/624,540	07/23/2003	Yoshinari Ichihashi	57810-070	7515
20277 75	90 06/08/2005	EXAMINER		
MCDERMOTT WILL & EMERY LLP			TRAN, MINH LOAN	
600 13TH STREET, N.W. WASHINGTON, DC 20005-3096			ART UNIT	PAPER NUMBER
			2826	
			DATE MAILED: 06/08/2005	

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)				
	10/624,540	ICHIHASHI ET AL.				
Office Action Summary	Examiner	Art Unit				
	Minh-Loan T. Tran	2826				
The MAILING DATE of this communication a Period for Reply A SHORTENED STATUTORY PERIOD FOR REP		·				
THE MAILING DATE OF THIS COMMUNICATION  - Extensions of time may be available under the provisions of 37 CFR after SIX (6) MONTHS from the mailing date of this communication.  - If the period for reply specified above is less than thirty (30) days, a r  - If NO period for reply is specified above, the maximum statutory perions  - Failure to reply within the set or extended period for reply will, by staff any reply received by the Office later than three months after the material patent term adjustment. See 37 CFR 1.704(b).	N. 1.136(a). In no event, however, may a reply reply within the statutory minimum of thirty (3 od will apply and will expire SIX (6) MONTH tute, cause the application to become ABAN	y be timely filed 30) days will be considered timely. S from the mailing date of this communication. IDONED (35 U.S.C. § 133).				
Status						
1) Responsive to communication(s) filed on 17	' March 2005.	•				
2a)⊠ This action is <b>FINAL</b> . 2b)□ Ti	This action is <b>FINAL</b> . 2b) ☐ This action is non-final.					
•	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.					
Disposition of Claims						
4)⊠ Claim(s) <u>8-19</u> is/are pending in the application	4) Claim(s) 8-19 is/are pending in the application.					
4a) Of the above claim(s) is/are withd	4a) Of the above claim(s) is/are withdrawn from consideration.					
5) Claim(s) is/are allowed.	6) Claim(s) 8-19 is/are rejected.					
6)⊠ Claim(s) <u>8-19</u> is/are rejected.						
·						
8) Claim(s) are subject to restriction and	d/or election requirement.					
Application Papers		•				
9)☐ The specification is objected to by the Exami	iner.					
10)☐ The drawing(s) filed on is/are: a)☐ accepted or b)☐ objected to by the Examiner.						
Applicant may not request that any objection to the	he drawing(s) be held in abeyance	e. See 37 CFR 1.85(a).				
Replacement drawing sheet(s) including the corre	• = • •					
11)☐ The oath or declaration is objected to by the	Examiner. Note the attached C	Office Action or form PTO-152.				
Priority under 35 U.S.C. § 119						
12)⊠ Acknowledgment is made of a claim for forei a)⊠ All b)□ Some * c)□ None of:	gn priority under 35 U.S.C. § 1	19(a)-(d) or (f).				
1.☐ Certified copies of the priority documents have been received.						
2. Certified copies of the priority docume		olication No. <u>09/788,673</u> .				
3. Copies of the certified copies of the pe	riority documents have been re	ceived in this National Stage				
application from the International Bure	eau (PCT Rule 17.2(a)).					
* See the attached detailed Office action for a li	ist of the certified copies not re	ceived.				
Attachment(s)						
1) Notice of References Cited (PTO-892)		nmary (PTO-413)				
<ul> <li>2) Notice of Draftsperson's Patent Drawing Review (PTO-948)</li> <li>3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/0</li> </ul>		Mail Date rmal Patent Application (PTO-152)				
Paper No(s)/Mail Date	6) Other:					

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#### **DETAILED ACTION**

## Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 8-15, 17-19 stand rejected under 35 U.S.C. 102(b) as being anticipated by Gocho, Tetsuo (JP 08-250,449).

With regard to claim 17, figure 2 of Gocho, Tetsuo discloses a method of fabricating a semiconductor device comprising the steps of forming an insulator film including a phosphorus silicate glass (PSG) upper insulator film 17 and a SiN lower insulation film 16 on a first conductive part 57 (or 56, 53); etching the insulator film thereby forming an opening 63 (or 62, 61) reaching the first conductive part 57 (or 56, 53); wherein the etching is conducted under substantially the same condition with respect to the upper insulator film 17 and the lower insulator film 16 i.e. the etching gas is  $C_4F_8$  and CO, the pressure of the etching atmosphere was set to 5.3 Pa and the temperature of the suscepter was set to 30° C (note paragraphs [0017] and [0019] of Gocho.) Figure 2 of Gocho, Tetsuo does not show a second conductive part connected to the first conductive part 57 (or 56, 53) through the opening 63 (or 62, 61). It is inherent that the second conductive part connected to the first conductive part 57 (or 56, 53) through the opening 63 (or 62, 61) so that the source electrode, the drain electrode and the gate electrode of the transistor can be formed.

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With regard to claims 18,19, 12, 13, 15, figure 2 and paragraph [0026]-[0028], [0030], and [0019] of Gocho, Tetsuo disclose the etching is conducted under a condition where a C-F based polymer ( $C_4F_8$ ) is formed when the first conductive part 57 (or 56, 53) is exposed to the etching.

With regard to claim 8, it is inherent that the lower SiN insulator film 16 has a higher etching selection ratio than the upper phosphorus silicate glass (PSG) insulator film 17 because the SiN insulator film 16 functions as an etch stopper (note paragraph [0021] of Gocho, Tetsuo.)

With regard to claim 9, figure 2 and paragraph [0013] of Gocho, Tetsuo disclose the first conductive part 57 (or 56, 53) contains a material having a higher etching selection ratio than the lower SiN conductive insulator 16.

With regard to claim 10, figure 2(1) of Gocho, Tetsuo discloses a step of silicifying the first conductive part 57 (or 56, 53) for forming silicide region 59 (or 58, 55) in advance of the step of forming the insulator film (16, 17).

With regard to claim 11, figure 2 (1-4) of Gocho, Tetsuo discloses the first conductive part include a gate electrode 53, a source region 56 and a drain region 57; and the step of silicifying the first conductive part includes a step of silicifying the surfaces of the gate electrode 53, the source region 56 and the drain region 57 in order to form the silicide regions 55 (gate), 58 (source) and 59 (drain).

With regard to claim 14, figure 2 of Gocho, Tetsuo discloses the SiN lower insulator film 16 is formed to be in contact with the surface of the first conductive part 57 (or 56, 53).

## Claim Rejections - 35 USC § 103

2. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claim 16 stand rejected under 35 U.S.C. 103(a) as being unpatentable over Gocho, Tetsuo (JP 08-250,449).

Gocho, Tetsuo does not disclose the lower insulator includes an SOG film.

Although Gocho, Tetsuo does not teach exact the material of the lower insulator film as that claimed by applicant, the material differences are considered obvious design choices and are not patentable unless unobvious or unexpected results are obtained from these changes. It appears that these changes produce no functional differences and therefore would have been obvious. Note *In re* Leshin, 125 USPQ 416, *In re Woodruff*, 919 F.2d 1575, 1578, 16 USPQ2d 1934, 1936 (Fed. Circ. 1990).

### Response to Arguments

Applicant's arguments filed 03/17/2005 have been fully considered but they are not persuasive.

It is argued, at pages 3 and 4 of the remarks, that "Gocho discloses a manufacturing process illustrated in Fig. 1 wherein etching of the second insulating film (an upper insulation film) 17 consisting of a PSG film and a first insulating film (a lower insulator film) 16 consisting of a SiN film are etched under **different**, repeat **different**, etching

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conditions from each other" or "the first insulating film 16 illustrated in Fig. 2 and the second insulating film 17 illustrated in Fig. 2 are conducted under the conditions as described in paragraphs [0017] and [0019], respectively, which are **different** from each

other" or "Gocho neither discloses nor suggests a method as claimed comprising the manipulative step of etching the upper insulator film and lower insulator film under the same condition." However, the paragraphs [0017] and [0019] of Gocho disclose the etching of the first insulating film 16 and the second insulating film 17 are conducted under the same condition such as the etching gas is C<sub>4</sub>F<sub>8</sub> and CO, the pressure of the etching atmosphere was set to 5.3 Pa and the temperature of the suscepter was set to 30° C. In response to applicant's argument that the references fail to show certain features of applicant's invention, it is noted that the features upon which applicant relies (i.e., the same **all conditions**) are not recited in the rejected claim 17. Although the claims are interpreted in light of the specification, limitations from the specification are not read into the claims. See In re Van Geuns, 988 F.2d 1181, 26 USPQ2d 1057 (Fed. Cir. 1993). Therefore, Applicant's claims 8-19 do not distinguish over the Gocho Tetsuo (JP 08-250,449) reference.

#### Conclusion

1. **THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within

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TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Minh-Loan T. Tran whose telephone number is (571) 272-1922. The examiner can normally be reached on Monday-Friday 9:00 AM-5:30 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Nathan J. Flynn can be reached on (571) 272-1915. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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Mlt 05/2005 Minh-Loan T. Tran Primary Examiner Art Unit 2826